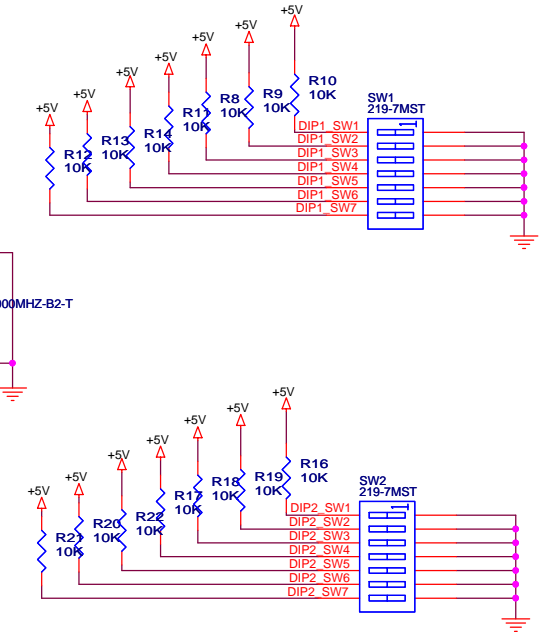
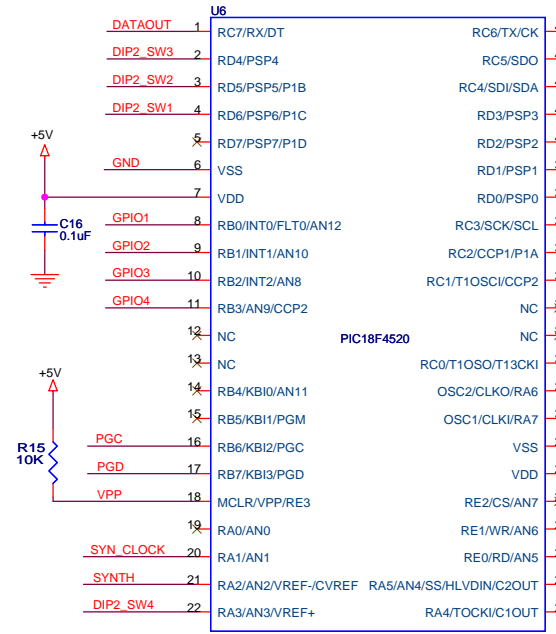
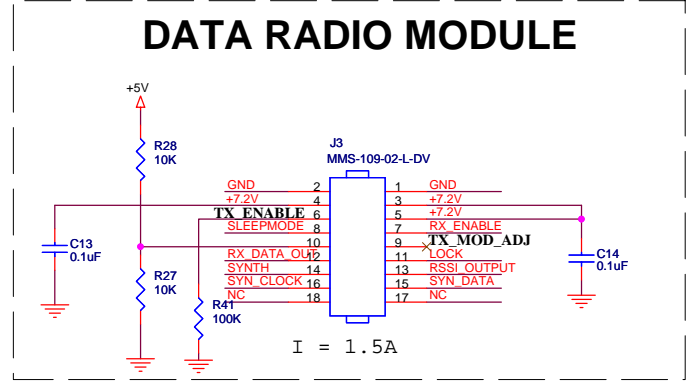
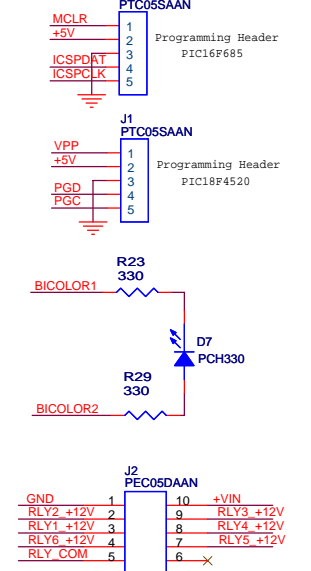
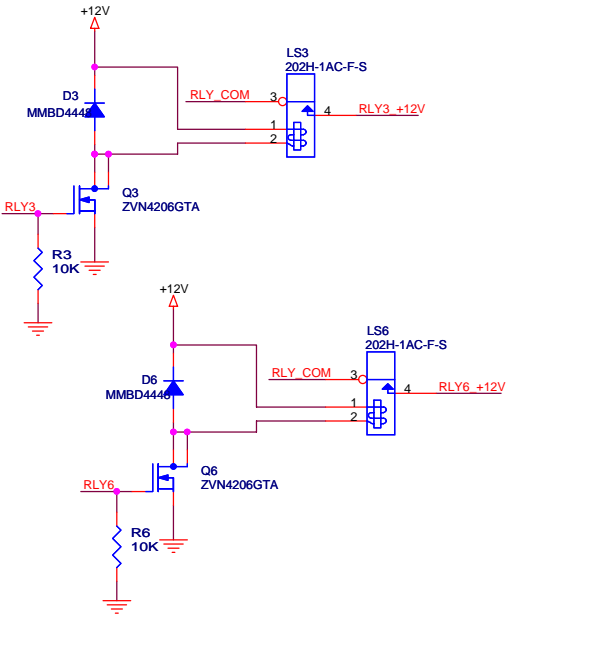
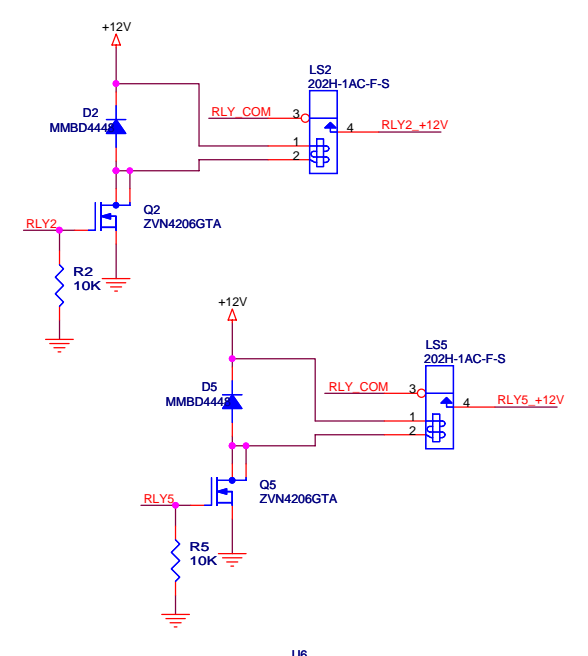
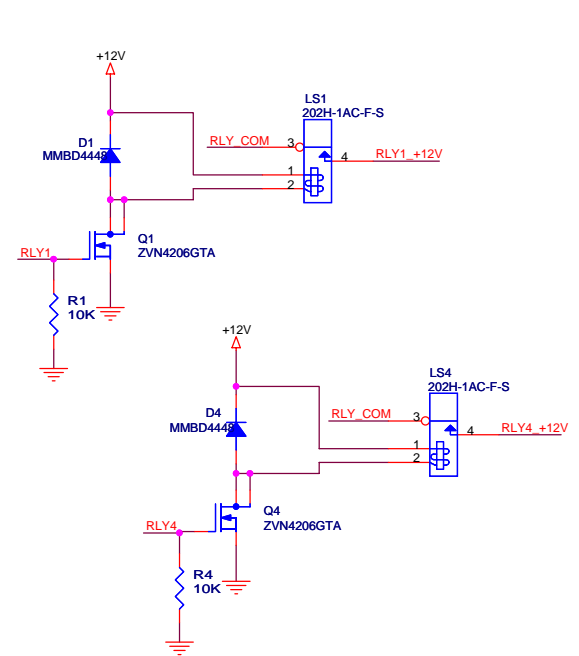


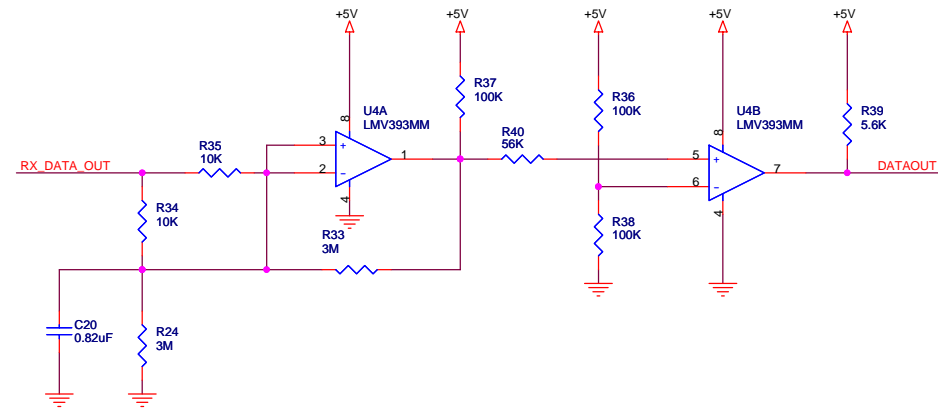
I/O Configuration

- +VIN
- GND
- +12V
- +7.2V
- +5V
- RLY* - Relay * control pin
- RLY_COM - Common connection between relays
- RLY*_+12V - Relay * input voltage
- LED - Led control pin
- DIP*_SW* - Dip switch control pin
- BUTTON - Input from push button
- RX_DATA_OUT - Output of the receiver baseband detected data
- RX_ENABLE - Receive enable pin
- SLEEPMODE - Puts the transceiver into a sleep mode
- LOCK - Synthesizer lock detect pin indication
- RSSI_OUTPUT - Receiver signal strength indicator
- SYNTH - Synthesizer enable pin
- SYN_CLOCK - Synthesizer clock
- SYN_DATA - Synthesizer data
- TX_MOD_ADJ - Modulation balance adjustment pin
- TX_MOD_IN - Transmit modulation input
- TX_ENABLE - Transmit enable pin



Design Specific Components

- DTC114YKA
- LMV393MM
- 202H-1AC-F-S
- MMS-109-02-L-DV
- MIC5235-5.0BM5
- LM350AT
- SMBJ58A-13
- M2MB-13-F
- 01110501H
- 225005
- ABLS-20.000MHz
- PIC16F877A
- 1N914TR
- ZVN4206GTA
- PCH330
- 219-7MST
- PIC16F685

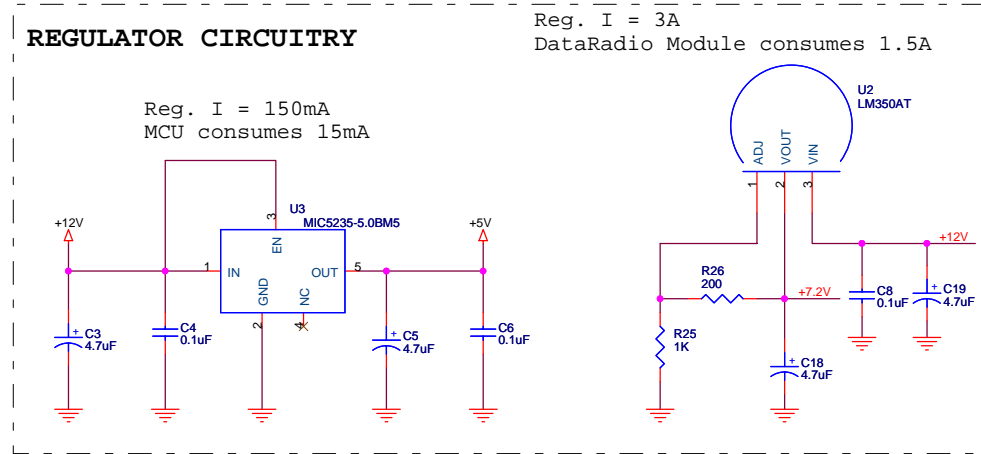


Design Notes:

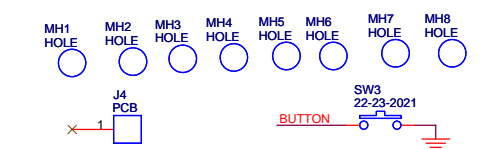
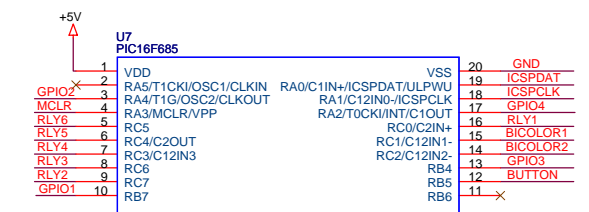
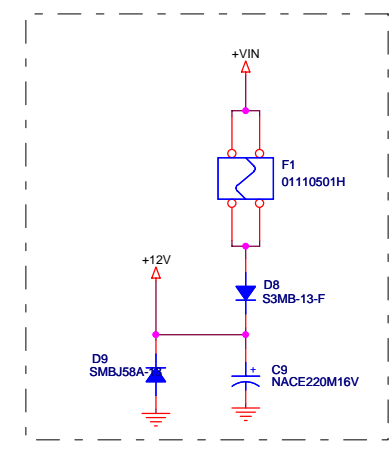
- Crystal tolerance and temperature
- Consider using ZVN4206A Fet vs. transistors

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 PIC Sales: Gerry Edwards 262-424-5511
 Micro Support: Jim Russell 612-859-0119
 Relays: Andy Keillor @ Larsen (414) 258-0529



REVERSE POLARITY AND TRANS. SUPPRESSION



DATE	INIT.	ECO Description

RFLABORATORIES	
Title: Priority Command RX	
Size: C	Document Number: Lucas Tech - Priority Command RX
Date: Monday, November 13, 2006	Sheet: 1 of 1